

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|--------|---|--|------------------|---------|------------------|
| L1 | 0 | ((bus) AND (pipeline) AND (firmware) AND (version) AND (interface) AND (memory storage)).clm. | US-PGPUB | OR | ON | 2007/12/18 11:53 |
| L2 | 0 | ((bus) AND (pipeline) AND (firmware) AND (version) AND (interface)).clm. | US-PGPUB | OR | ON | 2007/12/18 11:53 |
| L3 | 2 | ((bus) AND (pipeline) AND (firmware) AND (interface)).clm. | US-PGPUB | OR | ON | 2007/12/18 11:54 |
| L4 | 3 | ((bus) AND (pipeline) AND (firmware)).clm. | US-PGPUB | OR | ON | 2007/12/18 11:57 |
| L5 | 16 | ((pipeline) AND (firmware)).clm. | US-PGPUB | OR | ON | 2007/12/18 11:57 |
| L6 | 310 | rapid ADJ (io I?O) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/18 11:57 |
| L7 | 13 | L6 SAME FPGA | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/18 11:57 |
| L8 | 211343 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) (PLA) (programmable adj logic adj array) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2007/12/18 11:57 |
| L9 | 23 | ((pipeline (pipe adj line)) adj (unit computer processor)) SAME L8 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2007/12/18 11:57 |
| L10 | 50 | ("20040170070" "20060087883" "20060149859" "20060236292" "20060282254" "6085332" "6166960" "6301344" "6330667" "6587916" "6622245" "6850607" "6868349" "6948147" "6976155" "7055148" "20020000831" "20020041665" "20020129195" "20030018889" "20030084269" "20030217257" "20040179449" "20040255141" "20060098699" "20060101250" "20060178757" "20070067684" "20070192517" "4124889" "4443865" "4694398" "4701845" "5210854" "5218547" "5268928" "5450530" "5477264" "5579522" "5632026" "5638299" "5781921" "5784611" "5878257" "5951685" "6055632" "6112303" "6115443" "6170043" "6195592").pn. | US-PGPUB; USPAT | OR | ON | 2007/12/18 14:20 |
| L11 | 50 | ("20040130927" "20040133763" "20040136241" "20040170070" "20040181621" "5600844" "5745758" "5806068" "5832289" "5867712" "5983004" "6078981" "6233590" "5327550" "5457780" "5884089" "6032168" "4412286" "4541045" "4995041" "5218709" "5280474" "5313413" "5465368" "5471607" "5598408" "5657465" "5675757" "5784636" "5923575" "5933855" "6044460" "6163788" "6237021" "6247118" "4298950" "4399507" "4484349" "4490807" "4493048" "4534009" "4574348" "4594653" "4631662" "4755935" "4766564" "4793706" "4825388" "4850027" "4875161").pn. | US-PGPUB; USPAT | OR | ON | 2007/12/18 15:39 |
| S1 | 22075 | (FPGA (field adj programmable adj gate adj array)) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2006/03/28 10:41 |

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|-----|---------|---|---|----|-----|------------------|
| S2 | 511043 | firmware software | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:43 |
| S3 | 2676723 | memory register storage | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/14 09:29 |
| S4 | 154 | S1 NEAR5 S2 NEAR5 S3 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/14 09:35 |
| S5 | 2 | ("5583964" "4956771").pn. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/23 16:10 |
| S6 | 4 | ("5430687" "5844829" "6118938" "Re34363").PN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/23 16:10 |
| S7 | 522 | (716/16).CCLS. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/15 14:57 |
| S8 | 1245 | (710/8).CCLS. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/26 16:14 |
| S9 | 836 | (716/12).CCLS. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/15 14:57 |
| S10 | 354 | S7 and (FPGA (field adj programmable adj gate adj array)) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/15 14:49 |
| S11 | 55 | S7 and (FPGA (field adj programmable adj gate adj array)) and (((non adj volatile nonvolatile)) near3 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/15 15:18 |
| S12 | 28 | S8 and (FPGA (field adj programmable adj gate adj array)) and (((non adj volatile nonvolatile)) near3 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/15 15:25 |
| S13 | 36 | S9 and (FPGA (field adj programmable adj gate adj array)) and (((non adj volatile nonvolatile)) near3 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/15 15:26 |
| S14 | 11 | ("4080651" "5212693" "5430687" "5467286" "5606660" "5844829" "5859993" "5870609" "6009500" "6253281" "RE34363").PN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/23 16:11 |
| S15 | 7 | ("5327531" "5535355" "5537654" "5579522" "5630139" "5987581" "6012961").PN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/23 16:11 |
| S16 | 24167 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/22 15:00 |

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|-----|---------|---|---|----|----|------------------|
| S17 | 840637 | \$2program \$2configure | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/22 15:01 |
| S18 | 1736 | S17 NEAR5 S16 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/22 15:01 |
| S19 | 514536 | firmware software | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/22 15:01 |
| S20 | 2683755 | memory register storage | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/22 15:02 |
| S21 | 250 | (S17 NEAR5 S16) SAME S19 SAME S20 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:48 |
| S22 | 230 | S21 AND @ay<="2003" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/22 15:03 |
| S23 | 7 | ("5212693" "5430687" "5473758" "5841996" "5844829" "6222757" "RE34363").PN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/23 16:11 |
| S24 | 182936 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) (PLA) (programmable adj logic adj array) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:43 |
| S25 | 516952 | firmware software netlist (net adj list) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:43 |
| S26 | 24167 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:48 |
| S27 | 840637 | \$2program \$2configure | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:48 |
| S28 | 514536 | firmware software | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:48 |

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|-----|---------|---|---|----|-----|------------------|
| S29 | 2683755 | memory register storage | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:48 |
| S30 | 250 | (S27 NEAR5 S26) SAME S28 SAME S29 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:48 |
| S31 | 33 | ((S27 NEAR5 S24) SAME S25 SAME S29) NOT S30 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:49 |
| S32 | 27 | S31 AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:53 |
| S35 | 320 | xilinx.as. AND (S24 SAME S27) AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:55 |
| S36 | 87 | xilinx.as. AND (S24 SAME S27 SAME S25) AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 09:55 |
| S37 | 200 | (712/15).CCLS. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/23 12:55 |
| S38 | 676315 | registry database | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 16:16 |
| S39 | 516952 | firmware software netlist (net adj list) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 16:12 |
| S40 | 5240 | (S39 OR Configuration) ADJ S38 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 16:13 |
| S41 | 182936 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) (PLA) (programmable adj logic adj array) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 16:13 |
| S42 | 297 | S40 and S41 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 16:14 |

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|-----|---------|---|---|----|-----|------------------|
| S43 | 5 | S40 SAME S41 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/23 16:14 |
| S44 | 32 | ("4942319" "4963768" "5394031" "5394032" "5493239" "5543730" "5608342").PN. OR ("5838167").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/26 11:15 |
| S45 | 1503090 | processor CPU microcontroller (micro adj (controller processor)) microprocessor controller | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:30 |
| S46 | 5072 | test adj (port pin) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:30 |
| S47 | 3925 | JTAG | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:35 |
| S48 | 182936 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) (PLA) (programmable adj logic adj array) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:32 |
| S49 | 2 | S45 same S46 same S47 same S48 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:33 |
| S50 | 163 | S45 same S47 same S48 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 13:00 |
| S51 | 110 | S50 and @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:34 |
| S52 | 5736 | JTAG (joint adj test adj (action advisory) adj group) (boundary adj scan) (IEEE adj2 "1149.1") | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 12:59 |
| S53 | 138 | (S45 same S52 same S48) AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 13:01 |
| S54 | 1 | ("5892962").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/26 14:18 |
| S55 | 1 | ("1061439").PN. | EPO | OR | OFF | 2005/09/26 13:49 |
| S56 | 5 | FPGA ADJ Code ADJ Database | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/26 14:19 |

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|-----|-----|--|---|----|-----|------------------|
| S57 | 48 | ("20020070753" "20030005207" "20030120974" "4914576" "5101490" "5343478" "5425036" "5428800" "5465056" "5477544" "5761462" "5802268" "5826048" "5864486" "5894571" "5933614" "5935233" "5970005" "6031391" "6044025" "6055632" "6137738" "6167358" "6167477" "6198303" "6255849" "6289406" "6314550" "6366973" "6401153" "6405276" "6427198" "6430710" "6459297" "6460108" "6501682" "6526332" "6529989" "6567414" "6614259" "6622206" "6622246" "6629179" "6636927" "6651225" "6658508" "6658519" "6691205").PN. OR ("6918027").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/26 14:21 |
| S58 | 172 | (712/10).CCLS. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/26 16:14 |
| S59 | 119 | (multiprocessor (multi adj processor)) NEAR5 update | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 09:49 |
| S60 | 180 | (first ADJ processor) SAME (second ADJ processor) SAME update | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 09:54 |
| S61 | 103 | S59 AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 09:49 |
| S62 | 160 | S60 AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 09:54 |
| S63 | 47 | ((first ADJ processor) NEAR10 (second ADJ processor)) NEAR5 update | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 09:54 |
| S64 | 40 | S63 AND @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 09:54 |
| S65 | 1 | ("6425126").PN. | USPAT | OR | OFF | 2005/09/27 11:06 |
| S66 | 2 | separate NEAR5 small\$2 NEAR5 memor\$3 NEAR7 cost | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 11:07 |
| S67 | 139 | (first NEAR2 test NEAR2 port) SAME (second NEAR2 test NEAR2 port) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 12:27 |
| S68 | 5 | (first NEAR2 (test JTAG) NEAR2 port) SAME (second NEAR2 (test JTAG) NEAR2 port) SAME processor | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 12:29 |

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|-----|-----|--|---|----|-----|------------------|
| S69 | 0 | (processor NEAR2 (test JTAG) NEAR2 port) SAME ((FPGA PLA PLD FPLA) NEAR2 (test JTAG) NEAR2 port) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 12:34 |
| S70 | 106 | (processor NEAR2 (test JTAG) NEAR2 port) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 12:30 |
| S71 | 80 | S70 and @ay<="2002" | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 12:30 |
| S72 | 23 | ((FPGA PLA PLD FPLA) NEAR2 (test JTAG) NEAR2 port) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/27 12:34 |
| S73 | 24 | ("20020073372" "4771399" "5032533" "5652886" "5668816" "5702988" "5759877" "5878051" "5889701" "5909587" "5946267" "5970005" "5991907" "6003150" "6044025" "6046957" "6154864" "6202182" "6260139" "6401160" "6538468" "6539510" "6631487").PN. OR ("6839873").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/27 14:28 |
| S74 | 29 | ("5208781" "5394031" "5426379" "5430687" "5457408" "5485418" "5488582" "5654650" "5670897" "5726584" "5773993" "5804986" "5838165" "5870410" "5970240" "5977791" "6020757" "6026465" "6091263" "6097211" "6097988" "6104208" "6107821" "6157210" "RE34363").PN. OR ("6429682").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/27 15:56 |
| S75 | 47 | ("20020070753" "20030005207" "20030120974" "4914576" "5101490" "5343478" "5425036" "5428800" "5465056" "5477544" "5761462" "5802268" "5826048" "5864486" "5894571" "5933614" "5935233" "5970005" "6031391" "6044025" "6055632" "6137738" "6167358" "6167477" "6198303" "6255849" "6289406" "6314550" "6366973" "6401153" "6405276" "6427198" "6430710" "6459297" "6460108" "6501682" "6526332" "6529989" "6567414" "6614259" "6622206" "6622246" "6629179" "6636927" "6658508" "6658519" "6691205").PN. OR ("6883109").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/27 16:10 |
| S76 | 14 | ("5640107" "5646544" "5760607" "5768372" "5859544" "5870410" "5923614" "5999015" "6028445" "6031391").PN. OR ("6198303").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/09/27 16:20 |
| S77 | 807 | ((pipeline (pipe adj line)) adj (unit computer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/28 10:36 |
| S78 | 1 | hardware\$1 ADJ ((pipeline (pipe adj line)) adj (unit computer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/28 10:04 |
| S79 | 77 | (712/21).CCLS. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/09/28 10:17 |

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|-----|---------|--|---|----|-----|------------------|
| S80 | 182987 | (FPGA (field adj programmable adj gate adj array)) (programmable adj circuit) (PLA) (programmable adj logic adj array) | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/28 10:36 |
| S81 | 6 | ((pipeline (pipe adj line)) adj (unit computer)) SAME S80 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/28 10:38 |
| S82 | 1048 | ((pipeline (pipe adj line))) SAME S80 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/28 10:37 |
| S83 | 13 | ((pipeline (pipe adj line)) adj (unit computer processor)) SAME S80 | US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB | OR | ON | 2005/09/28 10:38 |
| S84 | 8801 | ((710/1,8,13,100,104) or (712/1,10,16,21,32,37) or (716/1,12,16,17)).CCLS. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/28 10:41 |
| S85 | 764271 | (firmware driver (operating near3 (instruction program software code))) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:43 |
| S86 | 561468 | (\$1PGA (programmable adj gate adj array)) (PLU (programmable adj logic adj unit)) (programmable near circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:43 |
| S87 | 4553329 | (memory stor\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:44 |
| S88 | 920918 | ((volatile near3 (memory storage)) RAM) (non-volatile near3 (memory storage)) (ROM EPROM EEPROM flash nv-ram) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:46 |
| S89 | 3098312 | (configur\$5 reconfigur\$5 initializ\$5 set?up provision\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:47 |
| S90 | 865 | S85 SAME S86 SAME S87 SAME S88 SAME S89 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:48 |
| S91 | 38 | S90 AND S84 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 10:49 |

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|----------|---------|--|---|----|-----|------------------|
| S92 | 8801 | ((710/1,8,13,100,104) or (712/1,10,16,21,32,37) or (716/1,12,16,17)).CCLS. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/28 13:32 |
| S93 | 764271 | (firmware driver (operating near3 (instruction program software code))) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:32 |
| S94 | 561468 | (\$1PGA (programmable adj gate adj array)) (PLU (programmable adj logic adj unit)) (programmable near circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:32 |
| S95 | 4553329 | (memory stor\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:32 |
| S96 | 920918 | ((volatile near3 (memory storage)) RAM) (non-volatile near3 (memory storage)) (ROM EPROM EEPROM flash nv-ram) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:32 |
| S97 | 3098312 | (configur\$5 reconfigur\$5 initializ\$5 set?up provision\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:32 |
| S98 | 9818215 | (multipl\$5 two three four many plural\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:35 |
| S99 | 24 | ((S98 NEAR2 S93) WITH S95) SAME S94 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 13:54 |
| S10 0 | 8801 | ((710/1,8,13,100,104) or (712/1,10,16,21,32,37) or (716/1,12,16,17)).CCLS. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/03/28 16:25 |
| S10 1 | 764271 | (firmware driver (operating near3 (instruction program software code))) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:50 |
| S10 2 | 561468 | (\$1PGA (programmable adj gate adj array)) (PLU (programmable adj logic adj unit)) (programmable near circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:50 |
| S10 3 | 4553329 | (memory stor\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:50 |

EAST Search History

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|----------|---------|---|--|----|----|------------------|
| S10 4 | 920918 | ((volatile near3 (memory storage)) RAM) (non-volatile near3 (memory storage)) (ROM EPROM EEPROM flash nv-ram) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:50 |
| S10 5 | 3098312 | (configur\$5 reconfigur\$5 initializ\$5 set?up provision\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:50 |
| S10 6 | 9818215 | (multipl\$5 two three four many plural\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:50 |
| S10 7 | 21 | (first WITH second WITH firmware) SAME S102 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/28 15:56 |
| S10 8 | 16 | ("5430687" "5844829" "6118938" "Re34363").PN. OR ("6308311").URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/03/28 16:20 |
| S10 9 | 5 | ("5784636" "5892962" "6049222" "6308311" "6326806").PN. | US-PGPUB; USPAT | OR | ON | 2006/10/30 18:24 |
| S11 0 | 2 | "20040170070".pn. | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/31 13:16 |
| S11 1 | 174 | (program\$4 configur\$5) NEAR3 (PLIC FPGA PLA PLU) NEAR3 (bus) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/31 13:40 |
| S11 2 | 1 | S111 SAME pipeline | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/31 13:39 |
| S11 3 | 76 | ((program\$4 configur\$5) NEAR3 (PLIC FPGA PLA PLU)) SAME (pipeline) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/31 13:40 |
| S11 4 | 1 | ("2004/0170070").URPN. | USPAT | OR | ON | 2006/10/31 13:46 |
| S11 5 | 2 | "20040170070".pn. | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/25 12:35 |

EAST Search History

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| S11 6 | 256 | rapid ADJ (io I?O) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 12:46 |
| S11 7 | 10 | S116 SAME FPGA | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 12:46 |
| S11 8 | 4 | (recieve download) NEAR2 (second new) NEAR2 (firmware configuration) NEAR2 (default original first) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 13:35 |
| S11 9 | 95 | (only) NEAR2 (second new) NEAR2 (firmware configuration) NEAR2 (default original first) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 13:35 |
| S12 0 | 3 | S119 AND FPGA | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 13:35 |
| S12 1 | 60 | (hardwire\$2 NEAR4 pipeline) | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 13:46 |
| S12 2 | 44 | ((JOHN) near2 (RAPP)).INV. | US-PGPUB; USPAT | OR | ON | 2007/07/27 14:02 |
| S12 3 | 46 | ((LARRY) near2 (JACKSON)).INV. | US-PGPUB; USPAT | OR | ON | 2007/07/27 14:02 |
| S12 4 | 300 | ((MARK) near2 (JONES)).INV. | US-PGPUB; USPAT | OR | ON | 2007/07/27 14:02 |
| S12 5 | 5 | ((TROY) near2 (CHERASARO)).INV. | US-PGPUB; USPAT | OR | ON | 2007/07/27 14:02 |
| S12 6 | 909098 | (firmware driver (operating near3 (instruction program software code))) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 14:09 |
| S12 7 | 646561 | (\$1PGA (programmable adj gate adj array)) (PLU (programmable adj logic adj unit)) (programmable near circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 14:09 |
| S12 8 | 5163854 | (memory stor\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 14:09 |

EAST Search History

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| S12 9 | 10813216 | (multipl\$5 two three four many plural\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 14:09 |
| S13 0 | 30 | ((S129 NEAR2 S126) WITH S128) SAME S127 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 14:09 |
| S13 1 | 347 | (hardware WITH pipeline) AND FPGA | US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/27 14:09 |
| S13 2 | 2 | ("6704816" "20040181621").pn. | US-PGPUB; USPAT | OR | ON | 2007/07/27 14:15 |
| S13 3 | 1 | ("20040181621").PN. | US-PGPUB | OR | ON | 2007/12/13 10:48 |
| S13 4 | 952470 | (firmware driver (operating near3 (instruction program software code))) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/14 11:08 |
| S13 5 | 670530 | (\$1PGA (programmable adj gate adj array)) (PLU (programmable adj logic adj unit)) (programmable near circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/14 11:08 |
| S13 6 | 5351671 | (memory stor\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/14 11:08 |
| S13 7 | 11114531 | (multipl\$5 two three four many plural\$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/14 11:08 |
| S13 8 | 31 | ((S137 NEAR2 S134) WITH S136) SAME S135 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/12/14 11:08 |
| S13 9 | 45 | ((JOHN) near2 (RAPP)).INV. | US-PGPUB; USPAT | OR | ON | 2007/12/14 11:52 |
| S14 0 | 46 | ((LARRY) near2 (JACKSON)).INV. | US-PGPUB; USPAT | OR | ON | 2007/12/14 11:52 |
| S14 1 | 314 | ((MARK) near2 (JONES)).INV. | US-PGPUB; USPAT | OR | ON | 2007/12/14 11:52 |
| S14 2 | 5 | ((TROY) near2 (CHERASARO)).INV. | US-PGPUB; USPAT | OR | ON | 2007/12/14 11:53 |